

Application No.: 10/728,262  
Reply to Office Action of April 15, 2005

**REMARKS/ARGUMENTS**

The above-identified patent application has been amended and reconsideration and re-examination are hereby requested.

Claims 1-3 and 8 have been canceled.

Claim 4 has been amended to include: "a resistor serially connected between a source of clock pulses and one of the input terminals of the differential amplifier". It is respectfully submitted that such is not described in either Tashiro et al., (USP 5,864,254) or Doblar et al, (U. S. Patent No. 6,640,309) taken either singly or in combination. Reconsideration is hereby requested.

Claim 10 has been re-written into independent form and has been indicated by the Examiner as being allowable,

Claim 11 has been amended to include: "a resistor serially connected between a source of clock pulses and one of the input terminals of the differential amplifier". It is respectfully submitted that such is not described in either Tashiro et al., (USP 5,864,254) or Doblar et al, (U. S. Patent No. 6,640,309) taken either singly or in combination. Reconsideration is hereby requested.

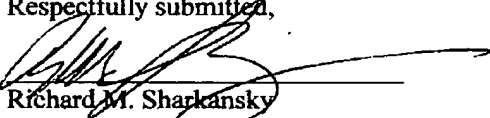
Claim 16 has been re-written into independent form, except for the removal of the phrase "the regeneration circuit" in line 1 of claim 12 to make the phrase grammatically correct, such claim 16 having been indicated by the Examiner as being allowable,

In the event any additional fee is required, please charge such amount to Patent and Trademark Office Deposit Account No. 05-0889.

Date

5-31-2005

Respectfully submitted,

  
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